


ANDHRA UNIVERSITY  
M.TECH  
DEPT. OF ELECTRONICS & COMMUNICATION ENGINEERING  
VLSI & EMBEDDED SYSTEMS  
W.E.F. 2023-2024 ADMITTED BATCH  
REGULAR  
I - SEMESTER  
TIME TABLE

**TIME: 9.00 AM TO 12.00 NOON**

Date	Day	Code	Subjects	Time	Max Marks
05-03-2024	Tuesday	44101	RTL Simulation and Synthesis with PLDs	3 Hrs	70
06-03-2024	Wednesday	44102	Embedded Systems	3 Hrs	70
07-03-2024	Thursday	44103 44104 44105	Elective- I Digital Design through Verilog VLSI Testing Programming Language for Embedded Software	3 Hrs	70
11-03-2024	Monday	44106 44107 44108	Elective -II System Design with Embedded Linux VLSI Signal Processing CAD of VLSI Systems	3 Hrs	70
12-03-2024	Tuesday	44109	Research and Methodology & IPR	3 Hrs	70
13-03-2024	Wednesday	44110	Audit Course	3 Hrs	70

(BY ORDER)

A.U.C.A.O.  
VISAKHAPATNAM  
DATE: 28-02-2024

  
(J. RATNAM)  
Deputy Registrar (Exams.)